

Day: Friday Date: 11/5/2004 Time: 15:57:27

### **Inventor Name Search Result**

Your Search was:

Last Name = GEFFKEN

First Name = ROBERT

Application#	Patent#	Status	Date Filed	Title	Inventor Name 45
60576439	Not Issued	020	06/03/2004	DUAL DAMASCENE INTEGRATION STRUCTURE AND METHOD FOR FORMING IMPROVED DUAL DAMASCENE INTEGRATION STRUCTURE	GEFFKEN, ROBERT M
<u>10687294</u>	Not Issued	030	10/16/2003	SINGLE AND MULTILEVEL REWORK	GEFFKEN, ROBERT M.
10632653	Not Issued	030	08/02/2003	CONTACT CAPPING LOCAL INTERCONNECT	GEFFKEN, ROBERT M.
<u>10605440</u>	Not Issued	030	09/30/2003	ADJUSTABLE SELF-ALIGNED AIR GAP DIELECTRIC FOR LOW CAPACITANCE WIRING	GEFFKEN, ROBERT M.
<u>10254277</u>	6746947	150	09/25/2002	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	GEFFKEN, ROBERT M
<u>10250147</u>	Not Issued	041	06/06/2003	FULL REMOVAL OF DUAL DAMASCENE METAL LEVEL	GEFFKEN, ROBERT M
10248637	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	GEFFKEN, ROBERT M
10248636	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	GEFFKEN, ROBERT M.
10248452	6674168	150	01/21/2003	SINGLE AND MULTILEVEL REWORK	GEFFKEN, ROBERT M
<u>10202134</u>	Not Issued	093	07/24/2002	SACRIFICIAL METAL SPACER DAMASCENE PROCESS	GEFFKEN, ROBERT M
<u>10186814</u>	Not Issued	080	07/01/2002	METAL SPACER IN SINGLE AND DUAL DAMASCENE PROCESSING	GEFFKEN, ROBERT M
<u>09854890</u>	6426558	150	05/14/2001	METALLURGY FOR SEMICONDUCTOR DEVICES	GEFFKEN, ROBERT M.
<u>09764504</u>	6548338	150	01/17/2001	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND	GEFFKEN, ROBERT M

				HEAT SINK	
<u>09745047</u>	6680514	150	12/20/2000	CONTACT CAPPING LOCAL INTERCONNECT	GEFFKEN, ROBERT M.
<u>09605806</u>	6503827	150	06/28/2000	METHOD OF REDUCING PLANARIZATION DEFECTS	GEFFKEN, ROBERT M.
09557833	Not Issued	083	04/26/2000	CORRECTION OF DEFECTS IN PLANARIZED SUBSTRATES	GEFFKEN, ROBERT M.
<u>09553581</u>	6300236	150	04/20/2000	COPPER STUD STRUCTURE WITH REFRACTORY METAL LINER	GEFFKEN, ROBERT M.
<u>09540737</u>	6452251	150	03/31/2000	DAMASCENE METAL CAPACITOR	GEFFKEN, ROBERT M.
<u>09527276</u>	6495917	150	03/17/2000	METHOD AND STRUCTURE OF COLUMN INTERCONNECT	GEFFKEN, ROBERT M.
<u>09526354</u>	6426249	150	03/16/2000	BURIED METAL DUAL DAMASCENE PLATE CAPACITOR	GEFFKEN, ROBERT M.
<u>09514511</u>	6420263	150	02/28/2000	METHOD FOR CONTROLLING EXTRUSIONS IN ALUMINUM METAL LINES AND THE DEVICE FORMED THEREFROM	GEFFKEN, ROBERT M
<u>09388314</u>	6498385	150	09/01/1999	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	GEFFKEN , ROBERT M.
09283828	6236103	150	03/31/1999	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND HEAT SINK	GEFFKEN , ROBERT M
09106980	6344373	150	06/29/1998	A METHOD FOR FORMING AN ANTIFUSE	GEFFKEN , ROBERT M.
<u>09080573</u>	6093630	150	05/18/1998	SEMI-CONDUCTOR PERSONALIZATION STRUCTURE AND METHOD	GEFFKEN , ROBERT MICHAEL
<u>09004557</u>	6180498	150	01/08/1998	ALIGNMENT TARGETS HAVING ENHANCED CONTRAST	GEFFKEN , ROBERT MICHAEL
<u>08941857</u>	6150723	150	09/30/1997	COPPER STUD STRUCTURE WITH REFRACTORY METAL LINER	GEFFKEN , ROBERT M
<u>08925655</u>	5795819	150	09/09/1997	INTEGRATED PAD AND FUSE STRUCTURE FOR PLANAR COPPER METALLURGY	GEFFKEN , ROBERT MICHAEL
<u>08858139</u>	<u>5985762</u>	150	05/19/1997	METHOD OF FORMING A SELF-ALIGNED COPPER DIFFUSION BARRIER IN VIAS	GEFFKEN, ROBERT M
08850033	<u>5811870</u>	150	05/02/1997	ANTIFUSE STRUCTURE	GEFFKEN,

					ROBERT M.
08801427	5898227	150	02/18/1997	ALIGNMENT TARGETS HAVING ENHANCED CONTRAST	GEFFKEN , ROBERT MICHAEL
08720852	<u>5719070</u>	150	10/02/1996	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>08686899</u>	5883435	150	07/25/1996	PERSONALIZATION STRUCTURE FOR SEMICONDUCTOR DEVICES	GEFFKEN , ROBERT M
08671903	5731624	150	06/28/1996	INTEGRATED PAD AND FUSE STRUCTURE FOR PLANAR COPPER METALLURGY	GEFFKEN , ROBERT M.
08557886	5585674	150	11/14/1995	TRANSVERSE DIFFUSION BARRIER INTERCONNECT STRUCTURE	GEFFKEN , ROBERT M
<u>08433276</u>	Not Issued	166	05/02/1995	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
08428995	5488013	150	04/26/1995	METHOD OF FORMING TRANSVERSE DIFFUSION BARRIER INTERCONNECT STRUCTURE	GEFFKEN , ROBERT M
<u>08182310</u>	5457345	150	01/14/1994	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>08169787</u>	Not Issued	166	12/20/1993	TRANSVERSE DIFFUSION BARRIER INTERCONNECT STRUCTURE	GEFFKEN, ROBERT M
<u>08060125</u>	Not Issued	161	05/07/1993	PLANARIZED LOCAL INTERCONNECT AND VIAS FOR SEMICONDUCTOR DEVICES	GEFFKEN , ROBERT M.
07984769	5339212	150	12/03/1992	SIDEWALL DECOUPLING CAPACITOR	GEFFKEN, ROBERT M
<u>07881097</u>	Not Issued	166	05/11/1992	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>07708608</u>	<u>5126006</u>	150	05/31/1991	PLURAL LEVEL CHIP MASKING	GEFFKEN , ROBERT M.
<u>07164692</u>	Not Issued	161		MULTI-LÉVEL METALLURGY FOR SEMICONDUCTOR DEVICES	GEFFKEN , ROBERT M.
<u>06269230</u>	4423547	150	06/01/1981	METHOD FOR FORMING DENSE MULTILEVEL	GEFFKEN , ROBERT M

		INTERCONNECTION METALLURGY FOR SEMICONDUCTOR DEVICES cords to Display.	
***************************************	Last Name	First Name	••••••
Search Another:	GEFFKEN	ROBERT	
Inventor		Search	

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### **Inventor Name Search Result**

Your Search was:

Last Name = HORAK

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>10752386</u>	Not Issued	030	01/06/2004	FORMATION OF A DISPOSABLE SPACER TO POST DOPE A GATE CONDUCTOR	HORAK, DAVID V.
10732958	Not Issued	030	12/11/2003	WRAP-AROUND GATE FIELD EFFECT TRANSISTOR	HORAK, DAVID VACLAV
<u>10732953</u>	Not Issued	030	12/11/2003	SHALLOW TRENCH ISOLATION FILL BY LIQUID PHASE DEPOSITION OF SIO2	HORAK, DAVID VACLAV
10732951	Not Issued	030	12/11/2003	SELECTIVE SYNTHESIS OF SEMICONDUCTING CARBON NANOTUBES	HORAK, DAVID VACLAV
10732582	Not Issued	030	12/10/2003	FIELD EMISSION PHASE CHANGE DIODE MEMORY	HORAK, DAVID V
10732580	Not Issued	030	12/10/2003	PHASE CHANGE TIP STORAGE CELL	HORAK, DAVID V.
10732579	Not Issued	030	12/10/2003	INTEGRATED CIRCUIT WITH UPSTANDING STYLUS	HORAK, DAVID V.
10707937	Not Issued	041	01/27/2004	DELIBERATE SEMICONDUCTOR FILM VARIATION TO COMPENSATE FOR RADIAL PROCESSING DIFFERENCES, DETERMINE OPTIMAL DEVICE CHARACTERISTICS, OR PRODUCE SMALL PRODUCTIONS	HORAK, DAVID V.
10707894	Not Issued	030	01/21/2004	LIQUID-FILLED BALLOONS FOR IMMERSION LITHOGRAPHY	HORAK, DAVID V
10707726	Not Issued	071	01/07/2004	VERTICAL CARBON NANOTUBE FIELD EFFECT TRANSISTOR	HORAK, DAVID V.
10707009	Not Issued	030	11/13/2003	ALTERNATING PHASE MASK BUILT BY ADDITIVE FILM DEPOSITION	HORAK, DAVID V.

<u>10705577</u>	Not Issued	030	11/10/2003	ETCH SELECTIVITY ENHANCEMENT FOR TUNABLE ETCH RESISTANT ANTI-REFLECTIVE LAYER	HORAK, DAVID V.
10703355	Not Issued	030		METHODS FOR FABRICATING A METAL-OXIDE-SEMICONDUCTOR DEVICE STRUCTURE AND METAL-OXIDE-SEMICONDUCTOR DEVICE STRUCTURES FORMED THEREBY	HORAK, DAVID VACLAV
10632653	Not Issued	030	:	CONTACT CAPPING LOCAL INTERCONNECT	HORAK, DAVID V.
10605766	Not Issued	071		METHOD OF FORMING GAS DIELECTRIC WITH SUPPORT STRUCTURE	HORAK, DAVID V.
<u>10604196</u>	Not Issued	030	06/30/2003	METHODS OF PLANARIZATION	HORAK, DAVID
10454852	Not Issued	071		METHOD FOR SUPPORTING A BOND PAD IN A MULTILEVEL INTERCONNECT STRUCTURE AND SUPPORT STRUCTURE FORMED THEREBY	HORAK, DAVID VACLAV
10448729	Not Issued	071	05/30/2003	DUAL GATE LOGIC DEVICE	HORAK, DAVID V.
10447876	Not Issued	041	05/29/2003	METHOD OF FORMING AN INTEGRATED SEMICONDUCTOR DEVICE HAVING CO-PLANAR DEVICE SURFACES	HORAK, DAVID V
10443709	6713835	150	05/22/2003	A MÜLTI-LEVEL INTERCONNECT	HORAK, DAVID VACLAV
<u>10159181</u>	6653737	150	05/31/2002	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	
<u>10141279</u>	6797641	150	05/08/2002	GATE OXIDE STABILIZATION BY MEANS OF GERMANIUM COMPONENTS IN GATE CONDUCTOR	HORAK, DAVID VACLAV
<u>10064316</u>	Not Issued	071	07/02/2002		HORAK, DAVID V
09779215	6429045	150	02/07/2001	STRUCTURE AND PROCESS FOR MULTI-CHIP CHIP ATTACH WITH REDUCED RISK OF ELECTROSTATIC DISCHARGE DAMAGE	HORAK, DAVID V.

09772378	6656807	150	01/30/2001	GROOVED PLANAR DRAM TRANSFER DEVICE USING BURIED POCKET	HORAK, DAVID
09745047	6680514	150	12/20/2000	CONTACT CAPPING LOCAL INTERCONNECT	HORAK, DAVID V.
<u>09718010</u>	6436814	150	11/21/2000	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	: AAKAAAAAAAAXAAXAAA
<u>09713830</u>	Not Issued	071	11/15/2000	FET WITH T-SHAPED GATE	HORAK, DAVID VACLAV
<u>09713766</u>	6358813	150	11/15/2000	METHOD FOR INCREASING THE CAPACITANCE OF A SEMICONDUCTOR CAPACITOR	HORAK, DAVID V
<u>09702338</u>	6583462	150	10/31/2000	VERTICAL DRAM HAVING METALLIC NODE CONDUCTOR	HORAK, DAVID VACLAV
09686740	6531724	150	10/10/2000	METHOD FOR FORMING BORDERLESS GATE STRUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V
<u>09667652</u>	6255158	150	09/22/2000	MANUFACTURING A VERTICAL DYNAMIC RANDOM ACCESS MENORY DEVICE	HORAK, DAVID V.
09657968	6420748	150	09/08/2000	BORDERLESS BITLINE AND WORDLINE DRAM STRUCTURE	HORAK, DAVID V
<u>09657232</u>	6627477	150	09/07/2000	MEHTOD OFASSEMBLING A PLURALITY OF SEMICONDUCTOR DEVICE HAVING DIFFERENT THICKNESS	HORAK, DAVID V.
<u>09651614</u>	6258661	150	08/30/2000	FORMATION OF OUT-DIFFUSED BITLINE BY LASER ANNEAL	HORAK, DAVID V
<u>09605806</u>	6503827	150	06/28/2000	METHOD OF REDUCING PLANARIZATION DEFECTS	HORAK, DAVID V.
09605322	6605534	150	06/28/2000	SELECTIVE DEPOSITION OF A CONDUCTIVE MATERIAL	HORAK, DAVID V.
<u>09604901</u>	6440801	150	06/28/2000	NOVEL STRUCTURE FOR FOLDED ARCHITECTURE PILLAR MEMORY CELL	HORAK, DAVID V.
09599783	Not Issued	092	06/22/2000	METHOD FOR ETCHING A SEMICONDUCTOR SUBSTRATE USING GERMANIUM HARD MASK	HORAK, DAVID V
<u>09565504</u>	6323082	150	05/05/2000	:1	HORAK, DAVID V.

				TRANSFER	
<u>09559363</u>	<u>6326275</u>	150	04/24/2000	DRAM CELL WITH VERTICAL	HORAK,
				CMOS TRANSISTOR	DAVID V.
09557833	Not Issued	083	04/26/2000	:1	HORAK,
00527024	Entrangement and the second se	121	03/20/2000	PLANARIZED SUBSTRATES	DAVID V.
09537934	Not Issued	161	03/29/2000	DEVICE CONTACT STRUCTURE AND METHOD FOR FABRICATING SAME	HORAK, DAVID V
<u>09532187</u>	6444402	150	03/21/2000	METHOD OF MAKING DIFFERENTLY SIZED VIAS AND LINES ON THE SAME LITHOGRAPHY LEVEL	HORAK, DAVID V.
<u>09524677</u>	6506653	150	03/13/2000	METHOD USING DISPOSABLE AND PERMANENT FILMS FOR DIFFUSION AND IMPLANT DOPING	HORAK, DAVID V
<u>09523839</u>	6333229	150	03/13/2000	METHOD FOR MANUFACTURING A FIELD EFFECT TRANSITOR (FET) HAVING MIS-ALIGNED-GATE STRUCTURE	HORAK, DAVID V.
<u>09523796</u>	6342323	150	03/13/2000	ALIGNMENT METHODOLOGY FOR LITHOGRAPHY	HORAK, DAVID VACLAY
<u>09523480</u>	6344416	150	03/10/2000	DELIBERATE SEMICONDUCTOR FILM VARIATION TO COMPENSATE FOR RADIAL PROCESSING DIFFERENCES, DETERMINE OPTIMAL DEVICE CHARACTERISTICS, OR PRODUCE SMALL PRODUCTIONS	HORAK, DAVID V.
<u>09494415</u>	6261933	150	01/31/2000	PROCESS FOR BUILDING BORDERLESS BITLINE, WORDLINE AND DRAM STRUCTURE	HORAK, DAVID V
<u>09493929</u>	6452265	150	01/28/2000	MULTI-CHIP MODULE UTILIZING A NONCONDUCTIVE MATERIAL SURROUNDING THE CHIPS THAT HAS A SIMILAR COEFFICIENT OF THERMAL EXPANSION	HORAK, DAVID V.
<u>09401054</u>	6441464	150	09/22/1999	GATE OXIDE STABILIZATION BY MEANS OF GERMANIUM COMPONENTS IN GATE CONDUCTOR	HORAK, DAVID VACLAV

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Search Another:	HORAK	DAVID
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### **Inventor Name Search Result**

Your Search was:

Last Name = HORAK

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
10904235	Not Issued	019	01/01/0001	APPARATUS AND METHOD FOR CONTROLLING PROCESS NON-UNIFORMITY	HORAK, DAVID V
10904200	Not Issued	019	01/01/0001	IRRADIATION ASSISTED REACTIVE ION ETCHING	HORAK, DAVID V.
<u>10892467</u>	Not Issued	030	07/15/2004	METHOD FOR FABRICATING STRAINED SEMICONDUCTOR STRUCTURES AND STRAINED SEMICONDUCTOR STRUCTURES FORMED THEREBY	<b>**</b> ********************
10879833	Not Issued	030	06/29/2004	DUAL GATED FINFET GAIN CELL	HORAK, DAVID VACLAV
<u>10879815</u>	Not Issued	030	06/29/2004	HORIZONTAL MEMORY GAIN CELLS	HORAK, DAVID VACLAV
10853177	Not Issued	030	05/26/2004	VERTICAL DUAL GATE FIELD EFFECT TRANSISTOR	HORAK, DAVID V.
10808740	Not Issued	030	03/25/2004	SYSTEM AND APPARATUS FOR PHOTOLITHOGRAPHY	HORAK, DAVID VACLAY
<u>10799819</u>	<u>6815737</u>	150	03/12/2004	METHOD FOR SELECTIVE TRIMMING OF GATE STRUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V.
<u>10798908</u>	Not Issued	030	03/11/2004	METHODS OF FORMING ALTERNATING PHASE SHIFT MASKS HAVING IMPROVED PHASE-SHIFT TOLERANCE	HORAK, DAVID VACLAV
<u>10787640</u>	Not Issued	030	02/26/2004	INTEGRATED CIRCUIT CHIP UTILIZING CARBON NANOTUBE COMPOSITE	HORAK, DAVID VACLAV

				INTERCONNECTION VIAS	
<u>10777576</u>	Not Issued	020	02/12/2004	METHODS OF FABRICATING VERTICAL CARBON NANOTUBE FIELD EFFECT TRANSISTORS FOR ARRANGEMENT IN ARRAYS AND FIELD EFFECT TRANSISTORS AND ARRAYS FORMED THEREBY	HORAK, DAVID VACLAV
<u>10767065</u>	Not Issued	030	01/29/2004	VERTICAL NANOTUBE SEMICONDUCTOR DEVICE STRUCTURES AND METHODS OF FORMING THE SAME	HORAK, DAVID VACLAV
10767039	Not Issued	030	01/29/2004	VERTICAL FIELD EFFECT TRANSISTORS INCORPORATING SEMICONDUCTING NANOTUBES GROWN IN A SPACER-DEFINED PASSAGE	HORAK, DAVID VACLAV
<u>10760500</u>	Not Issued	071	01/20/2004	METHOD FOR FORMING QUADRUPLE DENSITY SIDEWALL IMAGE TRANSFER (SIT) STRUCTURES	HORAK, DAVID VACLAV
<u>10752386</u>	Not Issued	030	01/06/2004	FORMATION OF A DISPOSABLE SPACER TO POST DOPE A GATE CONDUCTOR	HORAK, DAVID V
<u>10749638</u>	Not Issued	020	12/31/2003	MOVING LENS FOR IMMERSION OPTICAL LITHOGRAPHY	HORAK, DAVID VACLAV
<u>10711764</u>	Not Issued	019	01/01/0001	LOW-K DIELECTRIC MATERIAL BASED UPON CARBON NANOTUBES AND METHODS OF FORMING SUCH LOW-K DIELECTRIC MATERIALS	HORAK, DAVID V
<u>10711758</u>	Not Issued	020	10/04/2004	REDUCED MASK COUNT GATE CONDUCTOR DEFINITION	HORAK, DAVID V.
<u>10711458</u>	Not Issued	030	09/20/2004	SUB-LITHOGRAPHIC IMAGING TECHNIQUES AND PROCESSES	HORAK, DAVID V
<u>10710675</u>	Not Issued	030	07/28/2004	BORDERLESS CONTACT STRUCTURES	HORAK, DAVID V.
10710447	Not Issued	020	07/12/2004	FEATURE DIMENSION	HORAK, DAVID V

		XXXXXXXXX XXXXXXXXX		PROGRAM PRODUCT	
10709314	Not Issued	030	04/28/2004	METHOD FOR FORMING NARROW GATE STRUCTURES ON SIDEWALLS OF A LITHOGRAPHICALLY DEFINED SACRIFICIAL MATERIAL	HORAK, DAVID V.
10232072	6819841	150	08/29/2002		HORAK, DAVID V
10222035	Not Issued	071	08/16/2002	METHODS USING DISPOSABLE AND PERMANENT FILMS FOR DIFFUSION AND IMPLANTATION DOPING	HORAK, DAVID V.
<u>10187579</u>	Not Issued	041	07/01/2002	ng pananananananananananananananananananan	HORAK, DAVID V
<u>10013086</u>	6716647	150	12/07/2001	DELIBERATE SEMICONDUCTOR FILM VARIATION TO COMPENSATE FOR RADIAL PROCESSING DIFFERENCES, DETERMINE OPTIMAL DEVICE CHARACTERISTICS, OR PRODUCE SMALL PRODUCTIONS	HORAK, DAVID V.
10008092	6506660	150	11/13/2001	SEMICONDUCTOR CAPACITOR WITH NANOSCALE FEATURES	HORAK, DAVID V
<u>09992767</u>	6749969		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	REVERSE TONE PROCESS FOR MASKS	HORAK, DAVID V.
09989822	Not Issued	030	11/20/2001		HORAK, DAVID V
<u>09944665</u>	<u>6798017</u>	150	08/31/2001	::	HORAK, DAVID V.
<u>09879590</u>	6596597	150	06/12/2001	METHOD OF MANUFACTURING DUAL GATE LOGIC DEVICES	HORAK, DAVID V
09871926	6489207 6426558	150 150	06/01/2001 05/14/2001	AND CREATING A VERY SHALLOW SOURCE/DRAIN EXTENSION AND RESULTING SEMICONDUCTOR	HORAK, DAVID V. HORAK.

					SEMICONDUCTOR DEVICES	DAVID V.
	<u>09847479</u>	Not Issued	092	05/02/2001	GATE LINEWIDTH TAILORING AND CRITICAL DIMENSION CONTROL FOR SUB-100 NM DEVICES USING PLASMA ETCHING	HORAK, DAVID V.
	09824896	Not Issued	161	04/03/2001	PROCESS OF MANUFACTURING A DYNAMIC RANDOM ACCESS MEMORY DEVICE	HORAK, DAVID V.
	09793517	6429080	150	02/27/2001	MULTI-LEVEL DRAM TRENCH STORE UTILIZING TWO CAPACITORS AND TWO PLATES	HORAK, DAVID V.
	<u>09765561</u>	<u>6373091</u>	150	01/19/2001	VERTICAL DRAM CELL WITH TFT OVER TRENCH CAPACITOR	HORAK, DAVID VACLAV
100 100 100 100 100 100 100 100 100 100	<u>09394964</u>	6333533	150	09/10/1999	TRENCH STORAGE DRAM CELL WITH VERTICAL THREE-SIDED TRANSFER DEVICE	HORAK , DAVID V.
	<u>09385929</u>	6387596	150	08/30/1999	METHOD OF FORMING RESIST IMAGES BY PERIODIC PATTERN REMOVAL	HORAK , DAVID V
	09385907	6268908	150	08/30/1999	ILLUMINATION APERTURE	HORAK , DAVID VACLAY
	09384298	<u>6228706</u>	150	08/26/1999	VERTICAL DRAM CELL WITH TFT OVER TRENCH CAPACITOR	HORAK , DAVID VACLAV
	09379940	6207540	150	08/24/1999	METHOD FOR MANUFACTURING HIGH PERFORMANCE MOSFET DEVICE WITH RAISED SOURCE AND DRAIN	HORAK , DAVID V.
	09369412	6277543.	150	08/05/1999	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBIRD RESIST AND DEVICE FORMED THEREBY	HORAK, DAVID V
	09369082	6342722	150	:	ii :	HORAK , DAVID V.
	09368843	6420766 6255178		08/05/1999	TRANSISTOR HAVING RAISED SOURCE AND DRAIN	DAVID V.
š	09368767	6255178	120	08/05/1999	METHOD FOR FORMING	HORAK,

				TRANSISTORS WITH RAISED SOURCE AND DRAINS AND DEVICE FORMED THEREBY	DAVID V.
09299267	6387783	150	04/26/1999	METHODS OF T-GATE FABRICATION USING A HYBRID RESIST	HORAK, DAVID V
<u>09296807</u>	<u>6184549</u>	150	04/23/1999	TRENCH STORAGE DYNAMIC RANDOM ACCESS MEMORY CELL WITH VERTICAL TRANSFER DEVICE	HORAK, DAVID V
<u>09281136</u>	6037194	150	03/29/1999	METHOD FOR MAKING A DRAM CELL WITH GROOVED TRANSFER DEVICE	HORAK , DAVID V
<u>09275541</u>	6184151	150	03/24/1999	METHOD FOR FORMING CORNERED IMAGES ON A SUBSTRATE AND PHOTOMASK FORMED THEREBY	HORAK , DAVID V.
09253455	6426175	150	02/22/1999	FABRICATION OF A HIGH DENSITY LONG CHANNEL DRAM GATE WITH OR WITHOUT A GROOVED GATE	HORAK ; DAVID V

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### **Inventor Name Search Result**

Your Search was:

Last Name = HORAK

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>09479150</u>	6338921	150	01/07/2000	MASK WITH LINEWIDTHCOMPENSATION AND METHOD OF MAKING SAME	HORAK , DAVID V
09469275	6282115	150	12/22/1999	MULTI-LEVEL DRAM TRENCH STORE UTILIZING TWO CAPACITORS AND TWO PLATES	HORAK , DAVID V.
09469137	6333245	150	12/21/1999	METHOD FOR INTRODUCING DOPANTS INTO SEMICONDUCTOR DEVICES USING A GERMANIUM OXIDE SACRIFICIAL LAYER	HORAK , DAVID V.
09408353	Not Issued	161	09/29/1999	METHOD OF REDUCING CAPACITANCE IN THE WIRING STRUCTURES OF SEMICONDUCTOR DEVICES	HORAK , DAVID V.
09398659	6121128	150	09/17/1999	METHOD FOR MAKING BORDERLESS WORDLINE FOR DRAM CELL	HORAK, DAVID V
09394964	6333533	150	09/10/1999	TRENCH STORAGE DRAM CELL WITH VERTICAL THREE-SIDED TRANSFER DEVICE	::
<u>09374687</u>	6153902	150	08/16/1999	VERTICAL DRAM CELL WITH WORDLINE SELF-ALIGNED TO STORAGE TRENCH	HORAK , DAVID V
<u>09369004</u>	6245488	150	08/05/1999	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBRID RESIST AND DEVICE FORMED THEREBY	HORAK , DAVID V.
09296335	6297166	150	04/22/1999	METHOD FOR MODIFYING NESTED TO ISOLATED OFFSETS	HORAK, DAVID V

09287410	6376873	150	04/07/1999	VERTICAL DRAM CELL WITH ROBUST GATE-TO-STORAGE NODE ISOLATION	HORAK , DAVID V.
09243221	6228705	150	02/03/1999	OVERLAY PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE	HORAK, DAVID
<u>09192137</u>	6221562	150	11/13/1998	RESIST IMAGE REVERSAL BY MEANS OF SPUN-ON-GLASS	HORAK , DAVID V.
<u>09183338</u>	6150256	150	10/30/1998	METHOD FOR FORMING SELF-ALIGNED FEATURES	HORAK , DAVID V
<u>09182173</u>	6100172	150	10/29/1998	METHOD FOR FORMING A HORIZONTAL SURFACE SPACER AND DEVICES FORMED THEREBY	HORAK , DAVID V.
<u>09175304</u>	6090673	150	10/20/1998	DEVICE CONTACT STRUCTURE AND METHOD FOR FABRICATING SAME	HORAK , DAVID V
<u>09172865</u>	6063658	150	10/15/1998	TRENCH STORAGE DRAM CELL	HORAK , DAVID VACLAV
<u>09165503</u>	<u>5949700</u>	150	10/02/1998	FIVE SQUARE VERTICAL DYNAMIC RANDOM ACCESS MEMORY CELL	HORAK, DAVID V
<u>09136586</u>	6207493	150	08/19/1998	FORMATION OF OUT-DIFFUSED BITLINE BY LASER ANNEAL	HORAK , DAVID V.
09131828	<u>6319759</u>	150	08/10/1998	METHOD FOR MAKING OXIDE	HORAK , DAVID V
<u>09127132</u>	6221680	150	07/31/1998	PATTERNED RECESS FORMATION USING ACID DIFFUSION	HORAK , DAVID
09126412	<u>6121651</u>	150	07/30/1998	DRAM CELL WITH THREE-SIDED-GATE TRANSFER DEVICE	HORAK , DAVID V
<u>09110487</u>	6313492	150	07/06/1998	FREQUENCY DOUBLING HYBRID PHOTORESIST	HORAK , DAVID V.
09105739	6767789	150	06/26/1998	METHOD FOR INTERCONNECTION BETWEEN TRANSFER DEVICES AND STORAGE CAPACITORS IN MEMORY CELLS AND DEVICE FORMED THEREBY	HORAK, DAVID V
09093928	6114725	150	06/09/1998	STRUCTURE FOR FOLDED ARCHITECTURE PILLAR MEMORY CELL	HORAK , DAVID V.
<u>09092749</u>	6614074	150	06/05/1998	GROOVED PLANAR DRAM	HORAK,

				TRANSFER DEVICE USING BURIED POCKET	DAVID
<u>09089650</u>	6221704	150	06/03/1998	PROCESS FOR FABRICATING SHORT CHANNEL FIELD EFFECT TRANSISTOR WITH A HIGHLY CONDUCTIVE GATE	HORAK , DAVID V.
<u>09086174</u>	6190988	150	05/28/1998	\$	HORAK, DAVID
09086057	6225158	150	05/28/1998	TRENCH STORAGE DYNAMIC RANDOM ACCESS MEMORY CELL WITH VERTICAL TRANFER DEVICE	HORAK , DAVID V.
<u>09085965</u>	6107133	150	05/28/1998	METHOD FOR MAKING A FIVE SQUARE VERTICAL DRAM CELL	HORAK, DAVID V
<u>09082886</u>	6014422	250	05/21/1998	NOVEL METHOD FOR VARYING X-RAY HYBRID RESIST SPACE DIMENSIONS	HORAK , DAVID V.
09078118	6184041	150	05/13/1998	FUSED HYBRID RESIST SHAPES AS A MEANS OF MODULATING HYBRID RESIST SPACE WIDTH	HORAK , DAVID V
<u>09072031</u>	6210866	150	05/04/1998	METHOD FOR FORMING FEATURES USING SELF-TRIMMING BY SELECTIVE ETCH AND DEVICE FORMED THEREBY	HORAK , DAVID V.
<u>09071704</u>	Not Issued	161	05/01/1998		HORAK, DAVID V
<u>09071207</u>	6100013	150	05/01/1998	METHOD FOR FORMING TRANSISTORS WITH RAISED SOURCE AND DRAINS AND DEVICE FORMED THEREBY	HORAK , DAVID V.
09070633	6017810	150	04/30/1998	PROCESS FOR FABRICATING FIELD EFFECT TRANSISTOR WITH A SELF-ALIGNED GATE TO DEVICE ISOLATION	HORAK, DAVID V
<u>09056903</u>	<u>5945707</u>	150	04/07/1998	DRAM CELL WITH GROOVED TRANSFER DEVICE	HORAK , DAVID V.
09052538	6175128	150	03/31/1998	deneeeeeeeeeeekkaanaakkaankkakkkkkkkkkkk	HORAK, DAVID V
<u>09052403</u>	6271555	150	03/31/1998	BORDERLESS WORDLINE FOR	HORAK,

				DRAM CELL	DAVID V.
09024840	5998835	150	02/17/1998	HIGH PERFORMANCE MOSFET DEVICE WITH RAISED SOURCE AND DRAIN	::xxxxxxxxxxxxxxxxxxxxxxxxxx
09021924	6372412	150	02/10/1998	METHOD OF PRODUCING AN INTEGRATED CIRCUIT CHIP USING FREQUENCY DOUBLING HYBRID PHOTORESIST AND APPARATUS FORMED THEREBY	HORAK, DAVID V.
<u>09014523</u>	<u>5831301</u>	150	01/28/1998	TRENCH STORAGE DRAM CELL INCLUDING A STEP TRANSFER DEVICE	HORAK , DAVID VACLAV
08959779	6007968	150	10/29/1997	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBRID RESIST AND DEVICE FORMED THEREBY	HORAK , DAVID V.
08929730	<u>5956597</u>	150	09/15/1997	METHOD FOR PRODUCING SOI & NON-SOI CIRCUITS ON A SINGLE WAFER	HORAK , DAVID V
<u>08917009</u>	<u>5959325</u>	150	08/21/1997	METHOD FOR FORMING CORNERED IMAGES ON A SUBSTRATE AND PHOTOMASK FORMED THEREBY	HORAK , DAVID V.
<u>08878136</u>	<u>6391426</u>	150	06/19/1997	HIGH CAPACITANCE STORAGE NODE STRUCTURES	HORAK , DAVID V
08870336	<u>5953607</u>	150	06/06/1997	NOVEL BURIED STRAP FOR TRENCH STORAGE CAPACITORS IN DRAM TRENCH CELLS	HORAK , DAVID V.
<u>08790876</u>	<u>5776660</u>	150	02/03/1997	FABRICATION METHOD FOR HIGH-CAPACITANCE STORAGE NODE STRUCTURES	HORAK , DAVID V
08771570	Not Issued	161	12/20/1996	· j	HORAK , DAVID V.
<u>08715287</u>	6114082	150	09/16/1996	FREQUENCY DOUBLING HYBRID PHOTORESIST HAVING NEGATIVE AND POSITIVE TONE COMPONENTS AND METHOD OF PREPARING THE SAME	HORAK, DAVID V
07635819	<u>5173452</u>	150	01/02/1991	PROCESS FOR THE VAPOR DEPOSITION OF POLYSILANES PHOTORESISTS	HORAK , DAVID V.

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Inventor		Search	



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### **Inventor Name Search Result**

Your Search was:

Last Name = HORAK First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 8
09224760	6207514	150	01/04/1999	METHOD FOR FORMING BORDERLSS GATE STUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V
<u>09224759</u>	<u>6759315</u>	150	01/04/1999	METHOD FOR SELECTIVE TRIMMING OF GATE STRUCTURES AND APPARATUS FORMED THEREBY	HORAK , DAVID V.
09192140	6303272	150	11/13/1998	PROCESS FOR SELF-ALIGNMENT OF SUB-CRITICAL CONTACTS TO WIRING	HORAK, DAVID V
<u>09183926</u>	<u>6194268</u>	150	10/30/1998	PRINTING SUBLITHOGRAPHIC IMAGES USING A SHADOW MANDREL AND OFF-AXIS EXPOSURE	HORAK , DAVID V.
09182388	6096598	150	10/29/1998	METHOD FOR FORMING PILLAR MEMORY CELLS AND DEVICE FORMED THEREBY	HORAK , DAVID V
09182173	6100172	150	10/29/1998	METHOD FOR FORMING A HORIZONTAL SURFACE SPACER AND DEVICES FORMED THEREBY	HORAK , DAVID V.
<u>07704494</u>	Not Issued	163	05/23/1991	METHOD FOR ISOTROPIC DRY ETCHING OF A MULTI-LAYER STRUCTURE	HORAK , DAVID V
<u>07311326</u>	Not Issued	166	02/15/1989	PROCESS FOR THE VAPOR DEPOSITION OF POLYSILANES	HORAK , DAVID V.

Inventor Search Completed: No Records to Display.

	Last Name	First Name
Search Another:	HORAK	DAVID
Inventor		Search



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### **Inventor Name Search Result**

Your Search was:

Last Name = STAMPER First Name = ANTHONY

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>10707811</u>	Not Issued	071	01/14/2004	SACRIFICIAL INORGANIC POLYMER INTERMETAL DIELECTRIC DAMASCENE WIRE AND VIA LINER	STAMPER, ANTHONY K
<u>10707449</u>	Not Issued	041	12/15/2003	METHOD AND STRUCTURE FOR FORMING PRECISION MIM FUSIBLE CIRCUIT ELEMENTS USING FUSES AND ANTIFUSES	STAMPER, ANTHONY K.
10691881	Not Issued	020	10/23/2003	CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K
<u>10688387</u>	Not Issued	030	10/17/2003	:	STAMPER, ANTHONY K.
10687294	Not Issued	030	10/16/2003	SINGLE AND MULTILEVEL REWORK	STAMPER, ANTHONY K
<u>10659778</u>	Not Issued	061	09/10/2003 ·	-1	STAMPER, ANTHONY K.
<u>10640189</u>	Not Issued	071	08/13/2003	SPECIALLY SHAPED CONTACT VIA AND INTEGRATED CIRCUIT THEREWITH	STAMPER, ANTHONY KENDALL
10636110	Not Issued	090	08/07/2003	METHOD FOR CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
<u>10632653</u>	Not Issued	030	08/02/2003	CONTACT CAPPING LOCAL INTERCONNECT	STAMPER, ANTHONY K
10632652	Not Issued	041	08/02/2003	APPARATUS AND METHOD FOR FORMING A BATTERY IN AN INTEGRATED CIRCUIT	STAMPER, ANTHONY K.
<u>10605926</u>	Not Issued	071	11/06/2003	METHOD FOR REDUCING AMINE BASED CONTAMINANTS	STAMPER, ANTHONY K

10604609	Not Issued	030	08/04/2003	ELECTRICALLY TUNABLE ON-CHIP RESISTOR	STAMPER, ANTHONY KENDALL
<u>10604602</u>	Not Issued	094	08/04/2003	DAMASCENE INTERCONNECT STRUCTURES INCLUDING ETCHBACK FOR LOW-K DIELECTRIC MATERIALS	STAMPER, ANTHONY K
<u>10604572</u>	Not Issued	094	07/31/2003	ELECTRICAL DETECTION OF DICING DAMAGE	STAMPER, ANTHONY K
<u>10604367</u>	Not Issued	030	07/15/2003	METHOD FOR REDUCING FOREIGN MATERIAL CONCENTRATIONS IN ETCH CHAMBERS	STAMPER, ANTHONY KENDALL
<u>10604136</u>	Not Issued	030	06/27/2003	DETECTION OF HARDMASK REMOVAL USING A SELECTIVE ETCH	STAMPER, ANTHONY K.
10604134	6822472	150	06/27/2003	DETECTION OF HARD MASK REMAINING ON A SURFACE OF AN INSULATING LAYER	STAMPER, ANTHONY K
10604026	Not Issued	071		DUAL DAMASCENE INTERCONNECT STRUCTURES HAVING DIFFERENT MATERIALS FOR LINE AND VIA CONDUCTORS	STAMPER, ANTHONY
<u>10382736</u>	<u>6664581</u>	150		DAMASCENE CAPACITOR HAVING A RECESSED PLATE	STAMPER, ANTHONY K
10338624	Not Issued	071	01/07/2003	DUAL DAMASCENE COPPER INTERCONNECT TO A DAMASCENE TUNGSTEN WIRING LEVEL	STAMPER, ANTHONY K.
10250147	Not Issued	041	Bekekekekekekekek		STAMPER, ANTHONY K
<u>10186814</u>	Not Issued	080		METAL SPACER IN SINGLE AND DUAL DAMASCENE PROCESSING	STAMPER, ANTHONY K.
<u>10179285</u>	<u>6750114</u>	150		ONE-MASK METAL-INSULATOR-METAL CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY KENDALL
<u>10159181</u>	6653737	150		INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	STAMPER, ANTHONY K.
10063140	6452779	150		<b> </b>	STAMPER, ANTHONY KENDALL

10054605	Not Issued	133	11/13/2001	REDUCED ELECTROMIGRATION AND STRESSED INDUCED MIGRATION OF CU WIRES BY SURFACE COATING	STAMPER, ANTHONY KENDALL
10047964	Not Issued	164	01/15/2002	BILAYER HDP CVD / PE CVD CAP IN ADVANCED BEOL INTERCONNECT STRUCTURES AND METHOD THEREOF	STAMPER, ANTHONY K
<u>10046595</u>	6635548	150	10/26/2001	CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY K.
<u>09761123</u>	6650000	150	01/16/2001	APPARATUS AND METHOD FOR FORMING A BATTERY IN AN INTERGRATED CIRCUIT	STAMPER, ANTHONY K
<u>09753284</u>	6489663	150	01/02/2001	SPIRAL INDUCTOR SEMICONDUCTING DEVICE WITH GROUNDING STRIPS AND CONDUCTING VIAS	STAMPER, ANTHONY K.
09745047	6680514	150	12/20/2000	CONTACT CAPPING LOCAL INTERCONNECT	STAMPER, ANTHONY K
<u>09718010</u>	6436814	150	11/21/2000	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	STAMPER, ANTHONY K.
09712391	6730984	150	11/14/2000		STAMPER, ANTHONY K
<u>09693765</u>	<u>6647614</u>	150	10/20/2000	METHOD OF CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
09684000	6667533	150	03/11/2002	TRIPLE DAMASCENE FUSE	STAMPER, ANTHONY K
<u>09614234</u>	6545330	150	07/12/2000	ON CHIP ALPHA-PARTICLE DETECTOR	STAMPER, ANTHONY K.
09573877	Not Issued	161	05/19/2000	INTERCONNECTION WITH INTEGRATED CORROSION STOP	STAMPER, ANTHONY K
<u>09564626</u>	<u>6362531</u>	150	05/04/2000	RECESSED BOND PAD	STAMPER, ANTHONY K.
09557833	Not Issued	083	04/26/2000	CORRECTION OF DEFECTS IN PLANARIZED SUBSTRATES	STAMPER, ANTHONY K.
09541099	Not Issued	168	03/31/2000	CAPACITOR STRUCTURE AND METHOD OF MAKING SAME	STAMPER, ANTHONY K.
09540737	6452251	150	03/31/2000	DAMASCENE METAL CAPACITOR	STAMPER, ANTHONY K
09527858	<u>6368903</u>	150	03/17/2000	SOI LOW CAPACITANCE BODY	STAMPER,

				CONTACT	ANTHONY K.
<u>09527276</u>	6495917	150	03/17/2000	METHOD AND STRUCTURE OF COLUMN INTERCONNECT	STAMPER, ANTHONY K
<u>09526984</u>	Not Issued	168	03/16/2000	SHIELDED INTERCONNECTOR FOR AN INTERGRATED CIRCUIT DEVICE	STAMPER, ANTHONY K.
09526354	6426249	150	03/16/2000	BURIED METAL DUAL DAMASCENE PLATE CAPACITOR	STAMPER, ANTHONY K.
09525308	6444557	150	03/14/2000	METHOD OF FORMING A DAMASCENE STRUCTURE USING A SACRIFICIAL CONDUCTIVE LAYER AND THE STRUCTURE SO FORMED	STAMPER, ANTHONY K.
09525088	6680520	150	03/14/2000	METHOD AND STRUCTURE OF A PRECISION MIM FUSIBLE CIRCUIT ELEMENTS USING FUSES AND ANTIFUSES	STAMPER, ANTHONY K
<u>09514044</u>	6303456	150	02/25/2000	METHOD FOR MAKING A FINGER CAPACITOR WITH TUNEABLE DIELECTRIC CONSTANT	STAMPER, ANTHONY KENDALL
<u>09499577</u>	6384468	150	02/07/2000	CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY K
09489277	6521977	150	01/21/2000	DEUTERIUM RESERVOIRS AND INGRESS PATHS	STAMPER, ANTHONY K
09475712	6339022	150	12/30/1999	METHOD OF ANNEALING COPPER METALLURGY	STAMPER, ANTHONY K

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60429828</u>	Not Issued	159	11/27/2002	DUMMY ARC APPLY REWORK VIA FIRST DUAL DAMASCENE	STAMPER, ANTHONY K
60399991	Not Issued	159	07/31/2002	METHOD FOR REDUCING FOREIGN MATERIAL CONCENTRATIONS IN ETCH CHAMBERS	STAMPER, ANTHONY KENDALL
10849459	Not Issued	030	05/19/2004	SELF-ALIGNED CORROSION STOP FOR COPPER C4 AND WIREBOND	STAMPER, ANTHONY K.
10844533	Not Issued	030	05/13/2004	COPPER TO ALUMINUM INTERLAYER INTERCONNECT USING STUD AND VIA LINER	STAMPER, ANTHONY K.
10807029	Not Issued	041	03/23/2004	BILAYER HDP CVD / PE CVD CAP IN ADVANCED BEOL INTERCONNECT STRUCTURES AND METHOD THEREOF	STAMPER, ANTHONY K
<u>10753241</u>	Not Issued	041	01/08/2004	INCREASING AN ELECTRICAL RESISTANCE OF A RESISTOR BY OXIDATION OR NITRIDIZATION	STAMPER, ANTHONY K.
<u>10710828</u>	Not Issued	020	08/05/2004	COPPER ALLOY VIA BOTTOM LINER	STAMPER, ANTHONY K
10710478	Not Issued	030	07/14/2004	DUAL DAMASCENE WIRING AND METHOD	STAMPER, ANTHONY K.
10710210	Not Issued	030	06/25/2004	INTERCONNECT STRUCTURE IMPROVEMENTS	STAMPER, ANTHONY K.
<u>10710147</u>	Not Issued	* * * * * * * * * * * * * * * * * * *	06/22/2004	INTERLAYER CONNECTOR FOR PREVENTING DELAMINATION OF SEMICONDUCTOR DEVICE	STAMPER, ANTHONY K.

				AND METHODS OF FORMING SAME	
<u>10710034</u>	Not Issued	030	06/14/2004	PROCESS FOR INTERFACIAL ADHESION IN LAMINATE STRUCTURES THROUGH PATTERNED ROUGHING OF A SURFACE	STAMPER, ANTHONY K
<u>10710000</u>	Not Issued	020	06/11/2004	VARIABLE CONTACT METHOD AND STRUCTURE	STAMPER, ANTHONY K.
<u>10709747</u>	Not Issued	030	05/26/2004	EXPOSED PORE SEALING POST PATTERNING	STAMPER, ANTHONY K.
<u>10709722</u>	Not Issued	019	05/25/2004	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING AIR GAPS AND THE STRUCTURE SO FORMED	STAMPER, ANTHONY K.
10709115	Not Issued	030	04/14/2004	RESISTOR TUNING	STAMPER, ANTHONY K
<u>10709109</u>	Not Issued	020	04/14/2004	STRUCTURE AND METHOD FOR PROVIDING PRECISION PASSIVE ELEMENTS	STAMPER, ANTHONY K.
10709039	Not Issued	030	04/08/2004	METHOD FOR MANUFACTURING SELF-COMPENSATING RESISTORS WITHIN AN INTEGRATED CIRCUIT	STAMPER, ANTHONY KENDALL
10708883	Not Issued	030	03/30/2004	FILLED CAVITIES SEMICONDUCTOR DEVICES	STAMPER, ANTHONY K.
<u>10708735</u>	Not Issued	030	03/22/2004	CRACKSTOP WITH RELEASE LAYER FOR CRACK CONTROL IN SEMICONDUCTORS	STAMPER, ANTHONY K
<u>10707996</u>	Not Issued	030	01/30/2004	DEVICE AND METHODOLOGY FOR REDUCING EFFECTIVE DIELECTRIC CONSTANT IN SEMICONDUCTOR DEVICES	STAMPER, ANTHONY K.
<u>10702416</u>	Not Issued	030	11/06/2003	APPARATUS AND METHOD FOR LOW PRESSURE WIREBOND	STAMPER, ANTHONY K
10691881 ·	Not Issued	020	10/23/2003	CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
<u>10675177</u>	Not Issued	030	09/30/2003	TRIPLE DAMASCENE FUSE	STAMPER, ANTHONY K
<u>10650890</u>	Not Issued	041	08/28/2003	BILAYER HDP CVD/PE CVD CAP IN ADVANCED BEOL INTERCONNECT	STAMPER, ANTHONY K.

				STRUCTURES AND METHOD THEREOF	
10277835	<u>6770501</u>	150	10/23/2002	DEUTERIUM RESERVOIRS AND INGRESS PATHS	STAMPER, ANTHONY K
10254277	6746947	150	09/25/2002	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	STAMPER, ANTHONY K.
<u>10248637</u>	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	STAMPER, ANTHONY K
10248636	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	STAMPER, ANTHONY K.
<u>10248452</u>	<u>6674168</u>	150	01/21/2003	SINGLE AND MULTILEVEL REWORK	STAMPER, ANTHONY K.
<u>10248019</u>	<u>6797610</u>	150	12/11/2002	SUBLITHOGRAPHIC PATTERNING USING MICROTRENCHING	STAMPER, ANTHONY K.
<u>10246136</u>	6645789	150	09/18/2002	ON CHIP ALPHA-PARTICLE DETECTOR	STAMPER, ANTHONY K
<u>10202134</u>	Not Issued	093	07/24/2002	SACRIFICIAL METAL SPACER DAMASCENE PROCESS	STAMPER, ANTHONY K.
<u>10195763</u>	6677678	150	07/15/2002	A DAMASCENE STRUCTURE USING A SACRIFICIAL CONDUCTIVE LAYER	STAMPER, ANTHONY K
<u>10180777</u>	<u>6779711</u>	150	06/25/2002	SELF-ALIGNED CORROSION STOP FOR COPPER C4 AND WIREBOND	STAMPER, ANTHONY K.
10005951	<u>6650021</u>	150	12/03/2001	RECESSED BOND PAD	STAMPER, ANTHONY K
09996538	6420254	150	11/28/2001	RECESSED BOND PAD	STAMPER, ANTHONY K.
09996413	<u>6624475</u>	150	11/29/2001	SOI LOW CAPACITANCE BODY CONTACT	STAMPER, ANTHONY K
<u>09965289</u>	<u>6670255</u>	150	09/27/2001	METHOD OF FABRICATING LATERAL DIODES AND BIPOLAR TRANSISTORS	STAMPER, ANTHONY K.
09878117	Not Issued	094	06/08/2001		STAMPER, ANTHONY K
09866937	6593660	150	05/29/2001	PLASMA TREATMENT TO ENHANCE INORGANIC DIELECTRIC ADHESION TO COPPER	STAMPER, ANTHONY KENDALL
<u>09854890</u>	6426558	150	05/14/2001	************************************	STAMPER, ANTHONY K

<u>09853956</u>	6429524	150	05/11/2001	ULTRA-THIN TANTALUM NITRIDE COPPER INTERCONNECT BARRIER	STAMPER, ANTHONY K.
09832557	6522304	150	04/11/2001	DUAL DAMASCENE HORN ANTENNA	STAMPER, ANTHONY KENDALL
<u>09816977</u>	6566242	150	03/23/2001	DUAL DAMASCENE COPPER INTERCONNECT TO A DAMASCENE TUNGSTEN WIRING LEVEL	STAMPER, ANTHONY K.
09816278	6518643	150	03/23/2001	TRI-LAYER DIELECTRIC FUSE CAP FOR LASER DELETION	STAMPER, ANTHONY K.
<u>09811965</u>	6576525	150	03/19/2001	DAMASCENE CAPACITOR HAVING A RECESSED PLATE	STAMPER, ANTHONY K.
09811940	6563464	150	03/19/2001	INTEGRATED ON-CHIP HALF-WAVE DIPOLE ANTENNA STRUCTURE	STAMPER, ANTHONY K
09805027	Not Issued	041	03/12/2001	COPPER TO ALUMINUM INTERLAYER INTERCONNECT USING STUD AND VIA LINER	STAMPER, ANTHONY K.
<u>09788077</u>	6653710	150	02/16/2001	FUSE STRUCTURE WITH THERMAL AND CRACK-STOP PROTECTION	STAMPER, ANTHONY K
<u>09764504</u>	<u>6548338</u>	150	01/17/2001	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND HEAT SINK	STAMPER, ANTHONY K.
09681437	Not Issued	168	04/04/2001	METHOD AND APPARATUS FOR MAKING AIR GAP INSULATION FOR SEMICONDUCTOR DEVICES	STAMPER, ANTHONY KENDALL

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10904308	Not Issued	019	01/01/0001	METHOD AND APPARATUS FOR CORRECTION OF DEFECTS IN LITHOGRAPHY MASKS	STAMPER, ANTHONY K
<u>10711456</u>	Not Issued	030	09/20/2004	METHOD OF FABRICATING COPPER DAMASCENE AND DUAL DAMASCENE INTERCONNECT WIRING	STAMPER, ANTHONY K.
<u>10711394</u>	Not Issued	030	09/16/2004	AIR-GAP INSULATED INTERCONNECTIONS	STAMPER, ANTHONY K
<u>10710828</u>	Not Issued	020	08/05/2004	COPPER ALLOY VIA BOTTOM LINER	STAMPER, ANTHONY K.
<u>09472346</u>	<u>6261951</u>	150	12/27/1999	PLASMA TREATMENT TO ENHANCE INORGANIC DIELECTRIC ADHESION TO COPPER	STAMPER, ANTHONY KENDALL
<u>09471587</u>	Not Issued	168	12/23/1999	METHOD OF FABRICATING DUAL DAMASCENE WIRING	STAMPER , ANTHONY K.
09471065	Not Issued	133	12/22/1999	SEMICONDUCTOR DEVICE CONNECTIONS AND METHODS FOR MAKING THE SAME	STAMPER, ANTHONY K
09467207	Not Issued	161	12/20/1999	DUAL DAMASCENE INTERCONNECT STRUCTURE USING LOW STRESS FLOUROSILICATE INSULATOR WITH COPPER CONDUCTORS	STAMPER, ANTHONY K
09412708	6297149	150	10/05/1999	METHODS FOR FORMING METAL INTERCONNECTS	STAMPER, ANTHONY K
09388314	6498385	150	09/01/1999	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	STAMPER , ANTHONY K.

<u>09388163</u>	6403393	150	09/01/1999	A DEVICE HAVING	STAMPER,
				INTEGRATED OPTICAL AND COPPER CONDUCTORS AND METHOD OF FABRICATING SAME	ANTHONY K
<u>09388132</u>	6433429	150	09/01/1999	COPPER CONDUCTIVE LINE WITH REDUNDANT LINER AND METHOD OF MAKING	STAMPER , ANTHONY K
09361573	6342733	150	07/27/1999	REDUCED ELETROMIGRATION AND STRESSED INDUCED MIGRATION OF CU WIRES BY SURFACE COATING	STAMPER, ANTHONY KENDALL
09311973	6457234	150	05/14/1999	PROCESS FOR MANUFACTURING SELF-ALIGNED CORROSION STOP FOR COPPER C4 AND WIREBOND	STAMPER , ANTHONY K.
09283828	6236103	150	03/31/1999	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND HEAT SINK	STAMPER, ANTHONY K
09276160	6104079 ·	150	03/25/1999	CLOSELY PITCHED POLYSILICON FUSES AND METHOD OF FORMING THE SAME	STAMPER , ANTHONY K.
09257372	6214730	150	02/25/1999	FLUORINE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	
09225530	6255217	150	01/04/1999	PLASMA TREATMENT TO ENHANCE INORGANIC DIELECTRIC ADHESION TO COPPER	STAMPER , ANTHONY K.
09225477	<u>6331481</u>	150	01/04/1999	DAMASCENE ETCHBACK FOR LOW E DIELECTRIC	STAMPER, ANTHONY K
<u>09225116</u>	6734564	150	01/04/1999	SPECIALLY SHAPED CONTACT VIA AND INTEGRATED CIRCUIT THEREWITH	STAMPER , ANTHONY KENDALL
09225043	6261895	150	01/04/1999	POLYSILICON CAPACITOR HAVING LARGE CAPACITANCE AND LOW RESISTANCE AND PROCESS FOR FORMING THE CAPACITOR	STAMPER, ANTHONY K

	09224778	6319818	150	• • • • • • • • • • • • • • • • • • •	PATTERN FACTOR CHECKERBOARD FOR PLANARIZATION	STAMPER , ANTHONY K.
	09104074	5926738	150	06/24/1998	#	STAMPER, ANTHONY K
	09066121	6111301	150	:	INTERCONNECTION WITH INTEGRATED CORROSION STOP	STAMPER , ANTHONY K.
	08937622	<u>5930655</u>	150	09/25/1997	FLUORINE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	MARKARAKAN MAKAMAKAN MAKAMAKAN M
	08937367	6066577	150	09/25/1997		STAMPER , ANTHONY K.
	08903439	<u>5955773</u>	150		presentation	STAMPER , ANTHONY K
***************************************	08838371	<u>5808364</u>	150	;	INTERCONNECTS USING METAL SPACERS	STAMPER , ANTHONY K.
	08805023	<u>5851903</u>	150			STAMPER, ANTHONY K
	<u>08744846</u>	6310300	150		FLOURINE-FREE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	STAMPER , ANTHONY K.
	08744473	5773361	250	11/06/1996	PROCESS OF MAKING A MICROCAVITY STRUCTURE AND APPLICATIONS THEREOF	STAMPER, ANTHONY K
	08702082	<u>5907763</u>	250	08/23/1996	METHOD AND DEVICE TO MONITOR INTEGRATED TEMPERATURE IN A HEAT CYCLE PROCESS	STAMPER , ANTHONY K.
	08699830	Not Issued	166			STAMPER, ANTHONY K

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<u>08551092</u>	<u>5849629</u>	150	10/31/1995	METHOD OF FORMING A	STAMPER,		
				LOW STRESS POLYCIDE	ANTHONY K.		
	•			CONDUCTORS ON A			
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